



Subclass	Class	Issue Classification

JG-JT

U.S. UTILITY Patent Application

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/827015	D	716	17	282 316	Linh Nguyen

APPLICANTS
 Robert D. O'Bryan
 Patrick Hallinan
 Jung Lee
 Shridhar Mukund

Depopulated programmable logic array

ACQUINN

PTO-2040
 12/89

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____					
	(Assistant Examiner)	(Date)		ISSUE FEE	
	(Primary Examiner)	(Date)			
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due _____ Date Paid _____	
				ISSUE BATCH NUMBER	
	(Legal Instruments Examiner) _____ (Date) _____				

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.

Form PTO-438A
(Rev. 6/99)

FILED WITH: DISK (CRF) FICHE CD-ROM

(Attached in pocket on right inside flap)